

Department of Electronics and Communication Engineering

NIT Srinagar

Time Table (Tentative)

Semester: 7th B. Tech ECE

Classroom: L-15

w.e.f. 11-09-2025

Session: Autumn 2025

Section A

	9:00 - 9:50 am	9:50 - 10:40 am	10:40 - 11:30 am	11:30 - 12:20 pm	12:20 - 1:10 pm	1:10 - 2:00 pm	2:00 - 2:50 pm	2:50 - 3:40 pm	3:40 - 4:30 pm	4:30 - 5:20 pm
Monday		ME	ES	DSP	LUNCH					
Tuesday		MEL (G2)	ESPL (G2)				EMI			
Wednesday	WC	DSP	EMI				ES			
Thursday		MEL (G1)	EDA-II Lab				PPS			
Friday		ESPL (G1)	ME				WC	PPS		

Section B

	9:00 - 9:50 am	9:50 - 10:40 am	10:40 - 11:30 am	11:30 - 12:20 pm	12:20 - 1:10 pm	1:10 - 2:00 pm	2:00 - 2:50 pm	2:50 - 3:40 pm	3:40 - 4:30 pm	4:30 - 5:20 pm
Monday	WC	ESPL (G4)			LUNCH		ME	EMI		
Tuesday	DSP	WC	EMI				MEL (G4)			
Wednesday			MEL (G3)				ESPL(G3)			
Thursday	DSP	ES	ME				PPS			
Friday	ES	EMI	EDA-II Lab					PPS		

Course Title	Course Code	L	T	P	C	Faculty Incharge
Microwave Engineering (ME)	ECT 405	2	1	0	3	Dr. Mir Mohsina (Sec. A) / Dr. Amit Kumar (Sec. B)
Digital Signal Processing (DSP)	ECT 402	2	1	0	3	Dr. Omkar Singh (Sec. A) / Dr. Haroon (Sec. B)
Wireless Communication (WC)	ECT 403	2	1	0	3	Dr. Rajwinder Kaur (Sec. A & Sec. B)
Electronic Measurement & Instrumentation (EMI)	ECT 404	3	1	0	4	Dr. Gausi Qazi (Sec. A & Sec. B)
Embedded Systems (ES)	ECT 401	2	1	0	3	Dr. Mahroosh (Sec. A & Sec. B)
Embedded & Signal Processing Lab (ESPL)	ECL 407	0	0	2	1	Dr. Omkar Singh & Dr. Haroon (G1, G2, G3, G4)
EDA Tools-II Lab (EDA-II Lab)	ECL 408	0	0	2	1	Dr. S. A. Ahsan & Dr. Brajendra Singh (Sec. A & Sec. B)
Microwave Engineering Lab (MEL)	ECL 409	0	0	2	1	Dr. Amit Kumar & Dr. Mir Mohsina (G1,G2, G3, G4)
Project Pre-Work & Seminar (PPS)	ECP 406	0	0	4	3	Dr. Gausi Qazi / Dr. Mir Mohsina / Dr. Haroon / Concerned Supervisor

Department of Electronics and Communication Engineering

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Time Table (Tentative)

Semester: 5th B. Tech ECE

Classroom: L-14, L-15

w.e.f. 11-09-2025

Session: Autumn 2025

Section A

	9:00 - 9:50 am	9:50 - 10:40 am	10:40 - 11:30 am	11:30 - 12:20 pm	12:20 - 1:10 pm	1:10 - 2:00 pm	2:00 - 2:50 pm	2:50 - 3:40 pm	3:40 - 4:30 pm	4:30 - 5:20 pm
Monday	ED		CS-II		MP	LUNCH	MPL (G1)			IE-I (1)
Tuesday		VDL (G1)		MPL (G2)/ CSL-II (G1)			ED	MAT-V		IE-I (1)
Wednesday		MP		CS-II				E-I		IE-I (1)
Thursday					E-I		AWP	CSL-II (G2)		
Friday	E-I	AWP		MAT-V						

Section B

	9:00 - 9:50 am	9:50 - 10:40 am	10:40 - 11:30 am	11:30 - 12:20 pm	12:20 - 1:10 pm	1:10 - 2:00 pm	2:00 - 2:50 pm	2:50 - 3:40 pm	3:40 - 4:30 pm	4:30 - 5:20 pm
Monday	CSL-II (G4)					LUNCH	MAT-V	ED		IE-I* (2)
Tuesday	MAT-V		AWP				MPL (G4)			IE-I* (2)
Wednesday							AWP	E-I		IE-I* (2)
Thursday	CS-II		MP		E-I			ED		
Friday	E-I	CSL-II (G3)		MPL (G3)				CS-II	MP	

Course Title	Course Code	L	T	P	C	Faculty Incharge
Microprocessor (MP)	ECT 301	2	1	0	3	Prof. A. A. Mir (Sec. A & Sec. B)
Electronic Devices (ED)	ECT 302	2	1	0	3	Dr. S. A. Ahsan (Sec. A)/ Dr. Brajendra Singh (Sec. B)
Communication Systems-II (CS-II)	ECT 303	2	1	0	3	Dr. G.R.Begh (Sec.A) / Dr. Shahid Shah (Sec. B)
Antenna and Wave propagation (AWP)	ECT 304	2	1	0	3	Dr. Yusra Bandey (Sec. A & Sec. B)
Elective-I (E-I) Honours (VLSI Technology)	ECT 017	2	1	0	3	Prof. Najeed-ud-din
Mathematics V (MAT-V)	MAT 301	2	1	0	3	Prof. Niyaz Ahmad (Sec. A & Sec. B)
Institute Elective-I (IE-I)#	ECT 0xx	2	1	0	3	Dr. Kajal (Integ. Elec. & App.)/ Dr. Haroon (Consumer Electronics)
Microprocessor Lab (MPL)	ECL 306	0	0	2	1	Dr. Bisma Bilal (G1, G2, G3, G4) / Prof. A. A. Mir (G1, G3, G4) & Dr. S. A. Ahsan (G2)
Communication Systems-II Lab (CSL)	ECL 308	0	0	2	1	Dr. G. R. Begh (G1, G2, G3, G4) / Dr. Shahid (G1, G2) / Dr. Mahroosh (G3,G4)
# Institute Elective-I (IE-I)	1. Integrated Electronics & Applications 2. Consumer Electronics					

* Class will be held in L-15

Department of Electronics and Communication Engineering

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Time Table (Tentative)

Semester: 3rd B. Tech ECE

Classroom: PF-1

w.e.f. 11-09-2025

Session: Autumn 2025

Section A

	9:00 - 9:50 am	9:50 - 10:40 am	10:40 - 11:30 am	11:30 - 12:20 pm	12:20 - 1:10 pm	1:10 - 2:00 pm	2:00 - 2:50 pm	2:50 - 3:40 pm	3:40 - 4:30 pm	4:30 - 5:20 pm
Monday	MAT-III	ELC-I		PE	EEM	LUNCH	ELCL-I (G1) / CAL (G2)			
Tuesday				SSL (G2)			SS		MAT-III	
Wednesday	EEM		NA		SS		ELCL-I (G2)		ELC-I	
Thursday	PE		NA	CAL (G1)			SSL (G1)			
Friday										

Section B

	9:00 - 9:50 am	9:50 - 10:40 am	10:40 - 11:30 am	11:30 - 12:20 pm	12:20 - 1:10 pm	1:10 - 2:00 pm	2:00 - 2:50 pm	2:50 - 3:40 pm	3:40 - 4:30 pm	4:30 - 5:20 pm
Monday				CAL (G3)		LUNCH	SSL (G3)		MAT-III	
Tuesday	MAT-III		NA		PE					
Wednesday		ELCL-I (G3) /SSL (G4)					ELC-I	EEM	CAL (G4)	
Thursday		ELCL-I (G4)		SS			EEM			
Friday	ELC-I		NA	PE			SS			

Course Title	Course Code	L	T	P	C	Faculty Incharge
Electronics-I (ELC-I)	ECT 201	2	1	0	3	Dr. Ashok Kumar (Sec. A)/ Dr. Kajal (Sec. B)
Network Analysis (NA)	ECT 202	2	1	0	3	Mr. A. G. Mir (Sec. A & Sec. B)
Signals & Systems (SS)	ECT 203	2	1	0	3	Dr. Burhan Khursheed (Sec. A & Sec. B)
Philosophy for Engineers: Society, Culture and Ethics (PE)	HST009	2	1	0	3	HSSM Faculty (Sec. A & Sec. B)
Electronic Engineering Materials (EEM)	ECT 208	2	1	0	3	Dr. Bisma Bilal (Sec. A & Sec. B)
Mathematics III (MAT-III)	MAT 203	2	1	0	3	Prof. Abdul Liman (Sec. A & Sec. B)
Electronics-I Lab (ELCL-I)	ECL 204	0	0	2	1	Dr. Ashok Kumar & Dr. Kajal (G1, G2, G3, G4), Dr. Gausia Qazi (G2)
Circuit Analysis Lab (CAL)	ECL205	0	0	2	1	Mr. A. G. Mir (G1, G2, G3, G4) / Dr. Gausia Qazi (G1, G2) & Dr. S. A. Ahsan (G3, G4)
Signal & System Lab (SSL)	EEL 209	0	0	2	1	Dr. Burhan (G1,G2,G3, G4)/ Dr. Mahroosh (G1,G2) & Dr. Haroon (G4) & Dr. Shahid Mehraj Shah (G3)

Department of Electronics and Communication Engineering

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Time Table (Tentative)

Semester: 3rd M. Tech (ME & CSP)

Classroom: Committee Room

w.e.f. 11-09-2025

Session: Autumn 2025

	9:00 - 9:50 am	9:50 - 10:40 am	10:40 - 11:30 am	11:30 - 12:20 pm	12:20 - 1:10 pm	1:10 - 2:00 pm	2:00 - 2:50 pm	2:50 - 3:40 pm	3:40 - 4:30 pm	4:30 - 5:20 pm
Monday					MSD	LUNCH	ME (E-I) (STM)		(ACD-II)	
Tuesday	E-II (ES)		MSD	(ACD-II)				(E-I) STM		
Wednesday	ACA		E-II (ES)		MSD					
Thursday	CN		E-II (CSP)				(E-1) STC			
Friday	ACA	CN		(E-1) STC			E-II (CSP)			

Semester: 1st M. Tech (ME & CSP)

Classroom: Project Lab

Session: Autumn 2024

	9:00 - 9:50 am	9:50 - 10:40 am	10:40 - 11:30 am	11:30 - 12:20 pm	12:20 - 1:10 pm	1:10 - 2:00 pm	2:00 - 2:50 pm	2:50 - 3:40 pm	3:40 - 4:30 pm	4:30 - 5:20 pm
Monday		VD		VT	PE1	LUNCH	ME (E-II) STM			
Tuesday	E-II (ES)	PE1		VT			VD	ME (E-II) STM	VDL	
Wednesday	DC		E-II (ES)				CT			
Thursday			MF	ML			CSP (E-1) STC		CT	
Friday				CSP (E-1) STC	ML		MF		DC	

Course Title	Course Code	L	T	P	C	Faculty Incharge
Advanced Computer Architecture (ACA)	ECEM 104	2	1	0	3	Dr. Mir Mohsina
Computer Networks (CN)	ECEM 108	2	1	0	3	Dr. Rajwinder Kour
Special Topics in Microelectronics (E-I)	ECEM XXX	2	1	0	3	Dr. S. A. Ahsan
Analog CMOS Design-II (ACD-II)	ECEM 233	2	1	0	3	Dr. Pankaj Sharma
Digital Communications (DC)	ECEM 101	2	1	0	3	Dr. Mahroosh
Mathematical Foundation (MF)/ E-II (CSP) Special Topics in	ECEM 236	2	1	0	3	Dr. Omkar Singh
Machine Learning (ML)	ECEM xxx	2	1	2	3	Dr. Shahid Mehraj Shah
VLSI Technology (VT)	ECEM 164	2	1	2	3	Dr. Brajendra Singh
VLSI Design (VD)	ECEM 155	2	1	2	3	Dr. Amandeep Singh
Physical Electronics-I (PE-I)	ECEM 187	2	1	2	3	Prof. Najeed-ud-din
Mixed Signal Design (MSD)	ECEM 186	2	1	0	3	Dr. Ashok Kumar
Coding Techniques (CT)	ECEM xxx	0	0	4	2	Dr. Shahid Mehraj Shah & Brajendra Singh
VLSI Design Lab (VDL)	ECEM 111	0	0	2	1	Dr. Amandeep Singh & Dr. Burhan
Elective -I (E-1) Special Topics in Communication (STC)	ECEM 166	2	1	0	3	Dr. Amit Kumar
Elective-II (E-II) Embedded Systems (ES) ME 1rd Sem	ECEM 0xx	2	1	0	3	Dr. Kajal

